

The search for the Higgs Boson at CERN

Is there such a thing as too much compute power?

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The European Particle Physics Laboratory based in Geneva, Switzerland

Founded in 1954 by 12 countries for fundamental physics research in a post-war Europe

In 2012, it is a global effort of 20 member countries and scientists from 110 nationalities, working on the world's most ambitious physics experiments

~2'500 personnel, > 15'000 users
~1 bln CHF yearly budget



Mont Blanc (4,808m)

Geneva (pop. 190'000)

Lake Geneva (310m deep)

SUISSE
FRANCE

CMS

LHCb

ATLAS

CERN Meyrin

CERN Prévessin

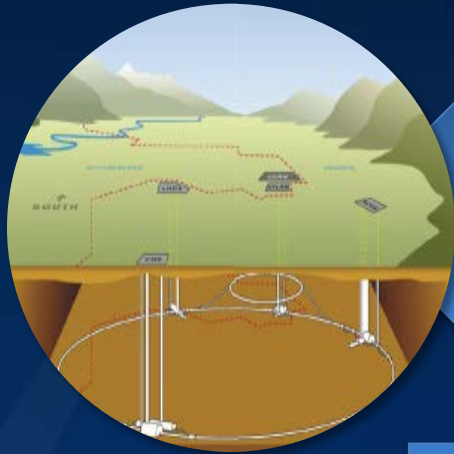
SPS 7 km

ALICE

LHC 27 km

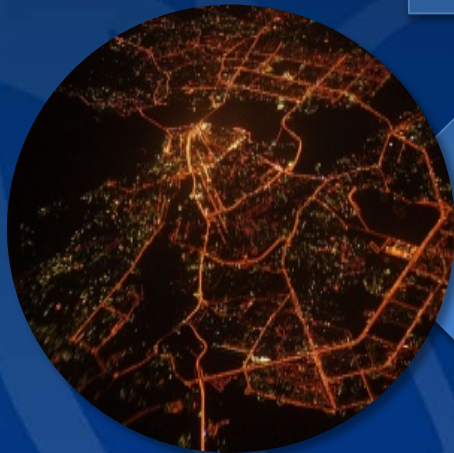
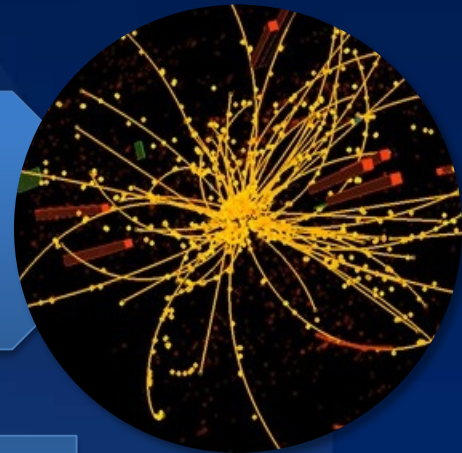


The Large Hadron Collider



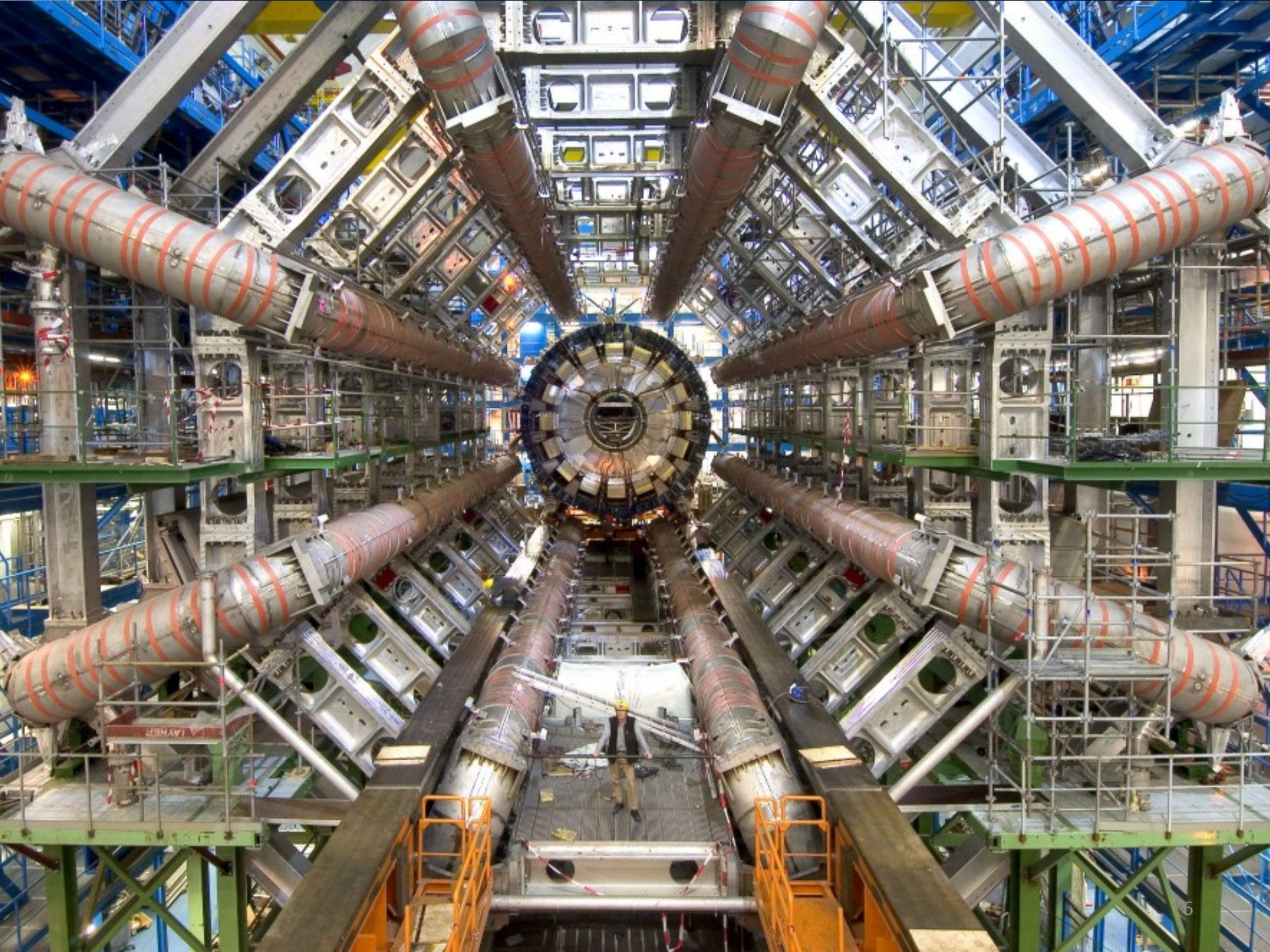
27 km underground
superconducting ring – possibly the
largest machine ever built by man

40 million collisions per second



150-200 MW power consumption

Andrzej Nowak - The search for the Higgs Boson at CERN - Is there such a thing as too much compute power?



Charged-particle multiplicities in pp interactions at $\sqrt{s} = 900$ GeV measured with the ATLAS detector at the LHC $\star, \star\star$

ATLAS Collaboration

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ABSTRACT

The first measurements from proton-proton collisions recorded with the ATLAS detector at the are presented. Data were collected in December 2009 using a minimum-bias trigger during coll at a centre-of-mass energy of 900 GeV. The charged-particle multiplicity, its dependence on trans momentum and pseudorapidity, and the relationship between mean transverse momentum and cha particle multiplicity are measured for events with at least one charged particle in the kinematic $|\eta| < 2.5$ and $p_T > 500$ MeV. The measurements are compared to Monte Carlo models of proton-p collisions and to results from other experiments at the same centre-of-mass energy. The charged-p multiplicity per event and unit of pseudorapidity at $\eta = 0$ is measured to be 1.333 ± 0.003 (stat 0.040 (sys)), which is 5–15% higher than the Monte Carlo models predict.

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1. Introduction

Inclusive charged-particle distributions have been measured in pp and $p\bar{p}$ collisions at a range of different centre-of-mass energy 13]. Many of these measurements have been used to constrain phenomenological models of soft-hadronic interactions and to properties at higher centre-of-mass energies. Most of the previous charged-particle multiplicity measurements were obtained by sel data with a double-arm coincidence trigger, thus removing large fractions of diffractive events. The data were then further correct remove the remaining single-diffractive component. This selection is referred to as non-single-diffractive (NSD). In some cases, desig as inelastic non-diffractive, the residual double-diffractive component was also subtracted. The selection of NSD or inelastic non-diffr charged-particle spectra involves model-dependent corrections for the diffractive components and for effects of the trigger selecti events with no charged particles within the acceptance of the detector. The measurement presented in this Letter implements a diffr physics, which uses a single-arm trigger overlapping with the acceptance of the tracking volume. Results are presented as incl inelastic distributions, with minimal model-dependence, by requiring one charged particle within the acceptance of the measurem

This Letter reports on a measurement of primary charged particles with a momentum component transverse to the beam dire $p_T > 500$ MeV and in the pseudorapidity range $|\eta| < 2.5$. Primary charged particles are defined as charged particles with a mean $|\tau| > 0.3 \times 10^{-10}$ s directly produced in pp interactions or from subsequent decays of particles with a shorter lifetime. The distribut tracks reconstructed in the ATLAS inner detector were corrected to obtain the particle-level distributions:

$$\frac{1}{N_{ev}} \frac{dN_{ch}}{d\eta} \frac{1}{d\eta} \frac{1}{N_{ev}} \frac{1}{2\pi p_T} \frac{d^2N_{ch}}{d\eta dp_T} \frac{1}{N_{ev}} \frac{dN_{ev}}{d\eta} \text{ and } \langle p_T \rangle \text{ vs. } n_{ch}$$

where N_{ev} is the number of events with at least one charged particle inside the selected kinematic range, N_{ch} is the total num charged particles, n_{ch} is the number of charged particles in an event and $\langle p_T \rangle$ is the average p_T for a given number of charged pa

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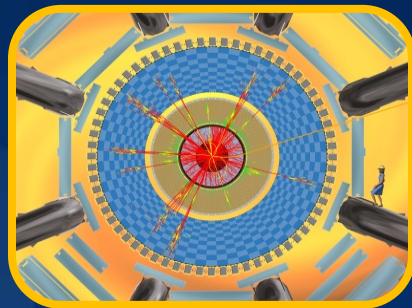
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⁴¹ TRIUMF⁴¹, 4004 Wesbrook Mall, Vancouver, BC V6T 2A3, York University⁴², Department of Physics and Astronomy, 4700 Keele St., Toronto, Ontario, M3J 1P3, Canada
⁴³ Yerevan Physics Institute, Akhikyan St., Yerevan, Armenia
⁴⁴ 1-1 Tennokuji, Tsukuba-shi, JP - Ibaraki 305-8571, Japan
⁴⁵ Tufts University, School of Technology Center, 4 Colby Street, Medford, MA 02155, United States
⁴⁶ Universidad Antonio Nariño, Centro de Investigaciones, Cra 3 Este No.47A-15, Bogotá, Colombia
⁴⁷ University of California, Irvine, Department of Physics & Astronomy, CA 92697-4575, United States
⁴⁸ INFN Gruppo Collegato di Udine⁴⁸, ICTP⁴⁹, Strada Costiera 11, I-34014 Trieste; Università di Udine, Dipartimento di Fisica⁵⁰, via delle Scienze 208, IT-33100 Udine, Italy
⁴⁹ University of Illinois, Department of Physics, 1110 West Green Street, Urbana, IL 61801, United States
⁵⁰ University of Jyväskylä, Department of Physics, PO Box 516, SF-20521 Jyväskylä, Finland
⁵¹ Instituto de Física Corpuscular (IFC), Centro Mixto UVEG-CSIC, Avda. 22085 ES-46071 Valencia, Dept. Física At. Mol. y Nuclear, Univ. de Valencia, and Instituto de Microelectrónica de Barcelona (IMB-CNM-CSIC), 08193 Bellaterra, Barcelona, Spain
⁵² University of British Columbia, Department of Physics, 6224 Agricultural Road, CA - Vancouver, BC V6T 1Z1, Canada
⁵³ University of Victoria, Department of Physics and Astronomy, PO Box 3055, Victoria, BC V8W 3A6, Canada
⁵⁴ Waseda University, Waseda Institute of Science, 2-17 Hachioji, Tokyo 192-8583, Japan
⁵⁵ The Weizmann Institute of Science, Department of Particle Physics, PO Box 26, 76100, Rehovot, Israel
⁵⁶ University of Wisconsin, Department of Physics, 1150 University Avenue, Madison, WI 53706, United States
⁵⁷ Julius-Maximilians-University of Würzburg, Physikalisches Institut, Am Hubland, 97074 Würzburg, Germany
⁵⁸ Bergische Universität, Fachbereich C, Physik, Postfach 100127, Gauss-Strasse 20, D-42097 Wuppertal, Germany
⁵⁹ Yale University, Department of Physics, PO Box 208121, New Haven, CT 06520-8121, United States
⁶⁰ INFN/AE, Viale Beni, Pavia 02, 40127 Pavia, Italy
⁶¹ INFN/AE, Via dei Sauro, 2, 00185 Roma, Italy
⁶² NERSC, Data Grid Facility, NERSC/Blue 2.1, 1DK-2770, Stockholm, Denmark

Data flow from the LHC detectors



Online triggering and filtering in detectors



Event simulation

Reconstruction

Selection and reconstruction

Raw Data (100%)

Event reprocessing

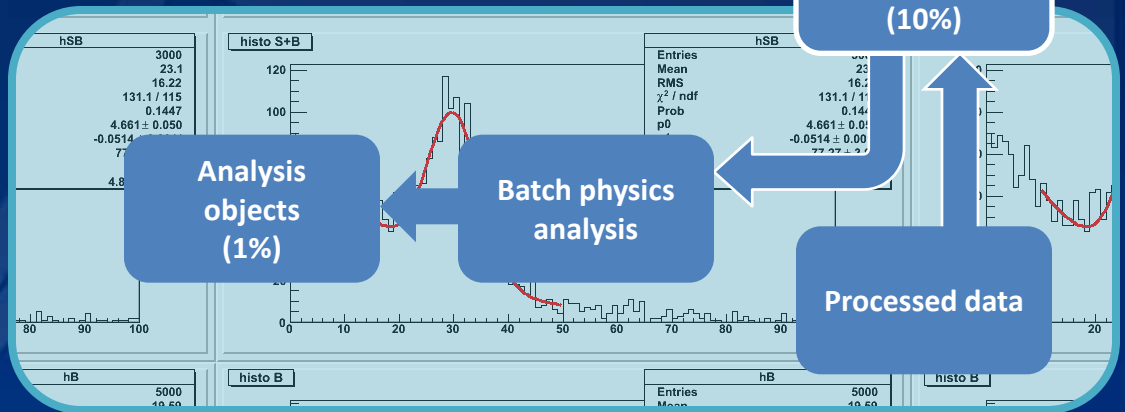
Event summary data (10%)

Analysis

Analysis objects (1%)

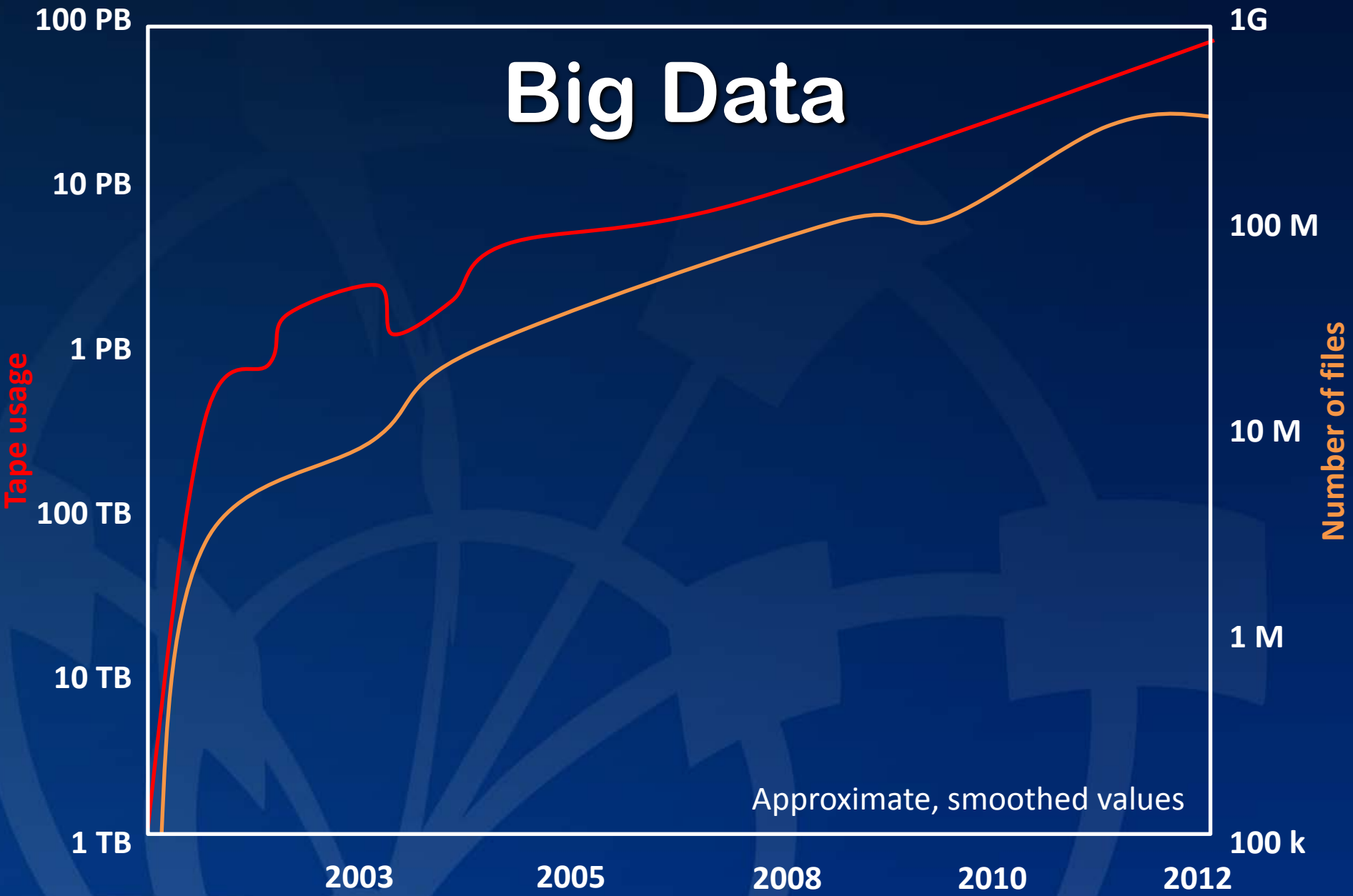
Batch physics analysis

Processed data



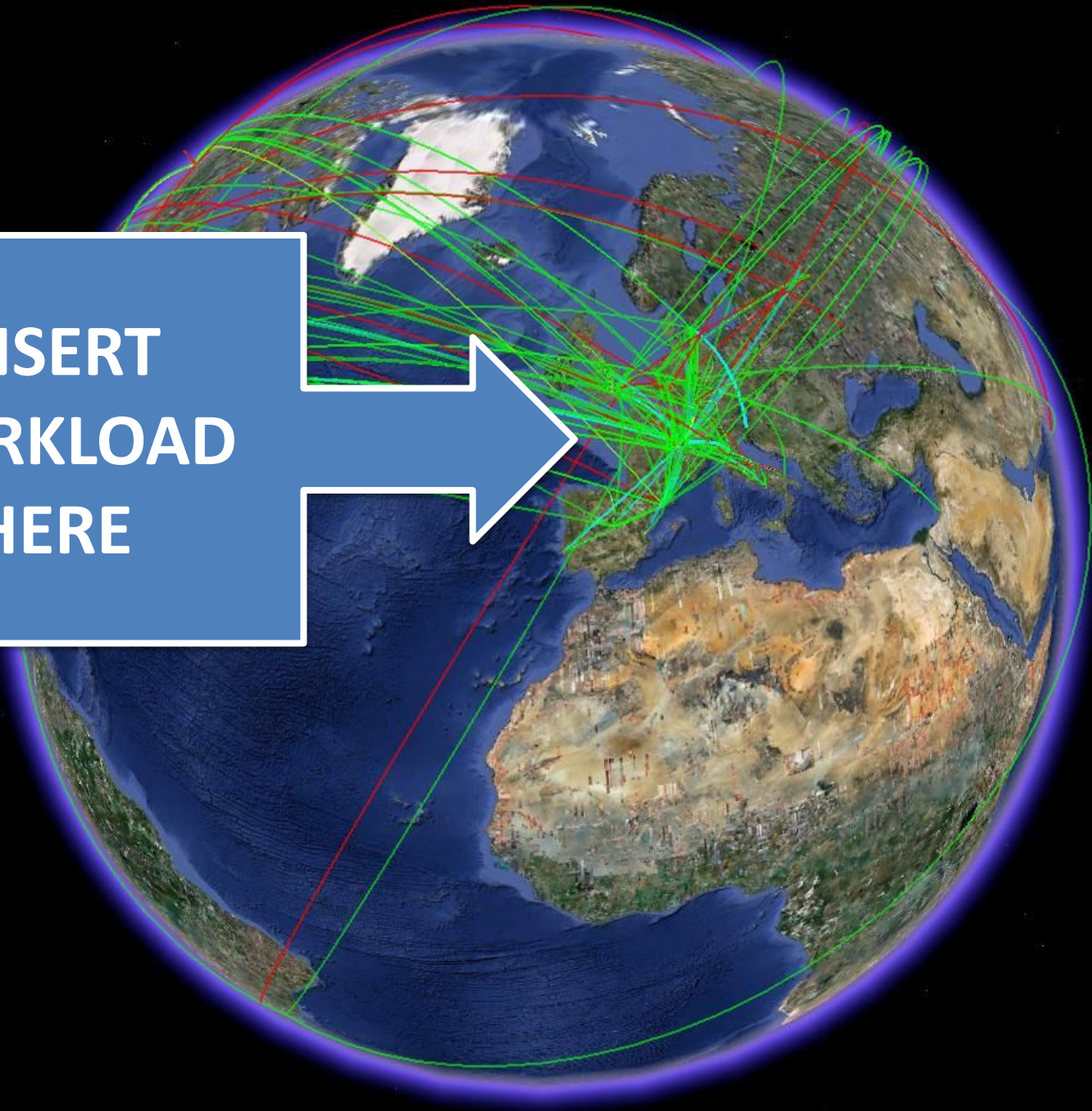
Andrzej Nowak - The search for the Higgs Boson at CERN - Is there such a thing as too much compute power?

Big Data



Approximate, smoothed values

**INSERT
WORKLOAD
HERE**



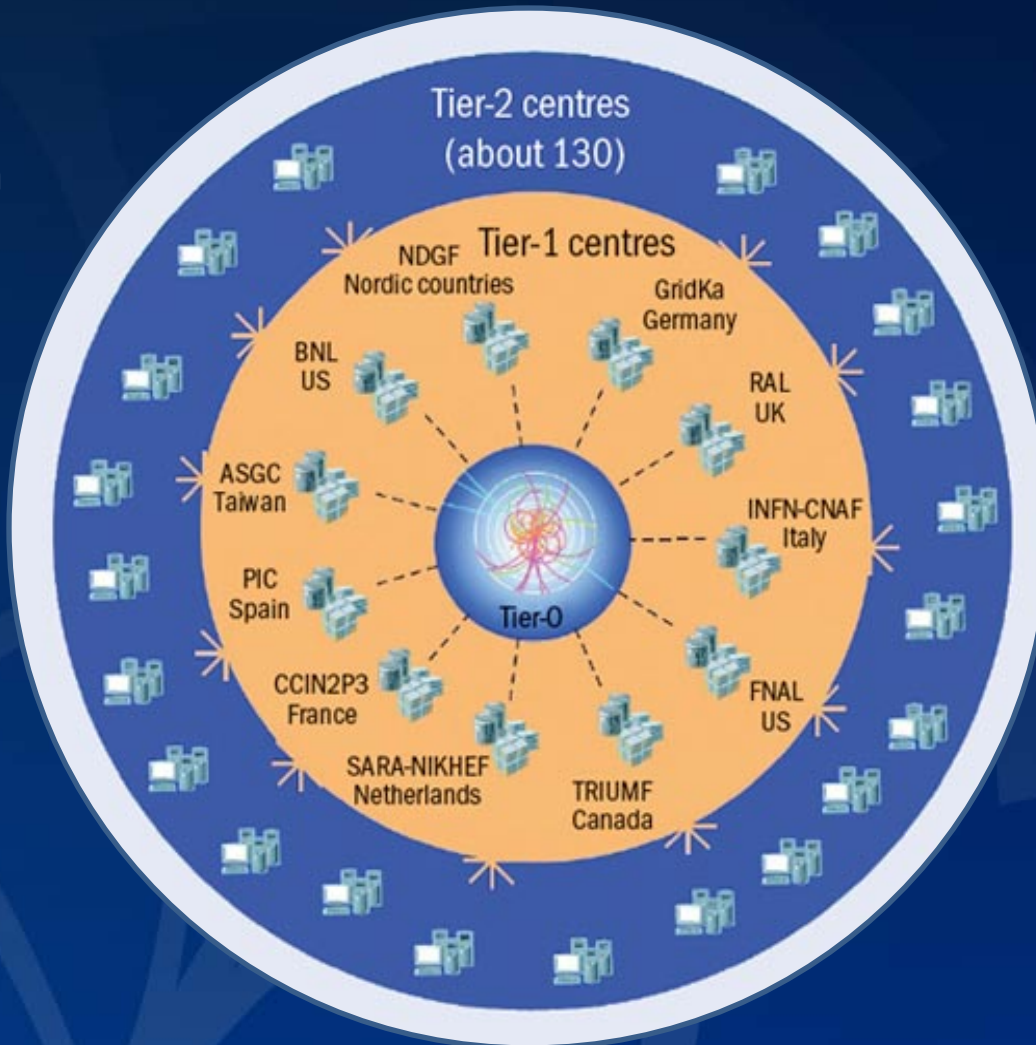
Collaboration on big data and computing

The Worldwide LHC Computing Grid

Tier-0 (CERN): data recording, reconstruction and distribution

Tier-1: permanent storage, re-processing, analysis

Tier-2: Simulation, end-user analysis

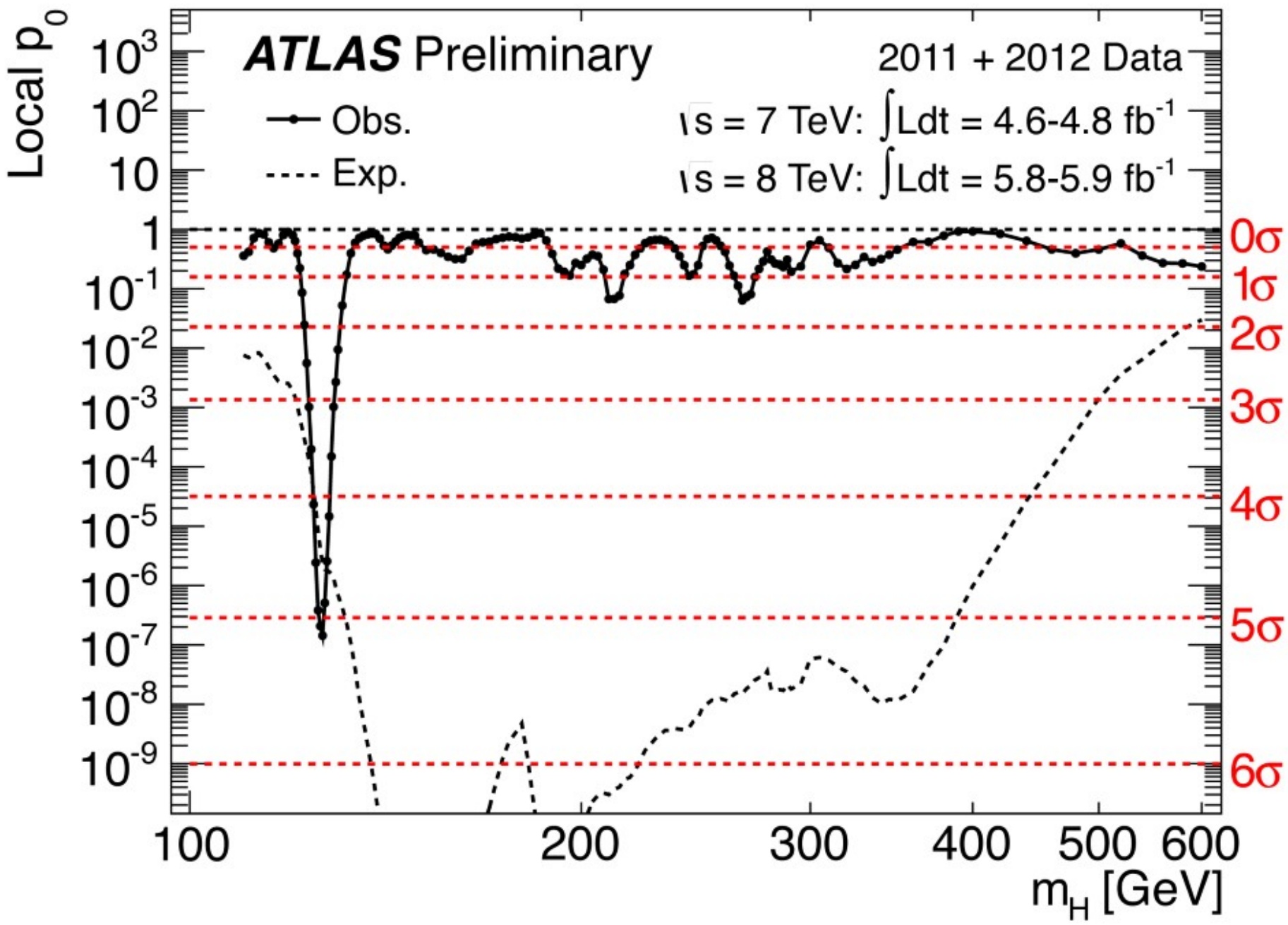


nearly 160 sites

~250'000 cores

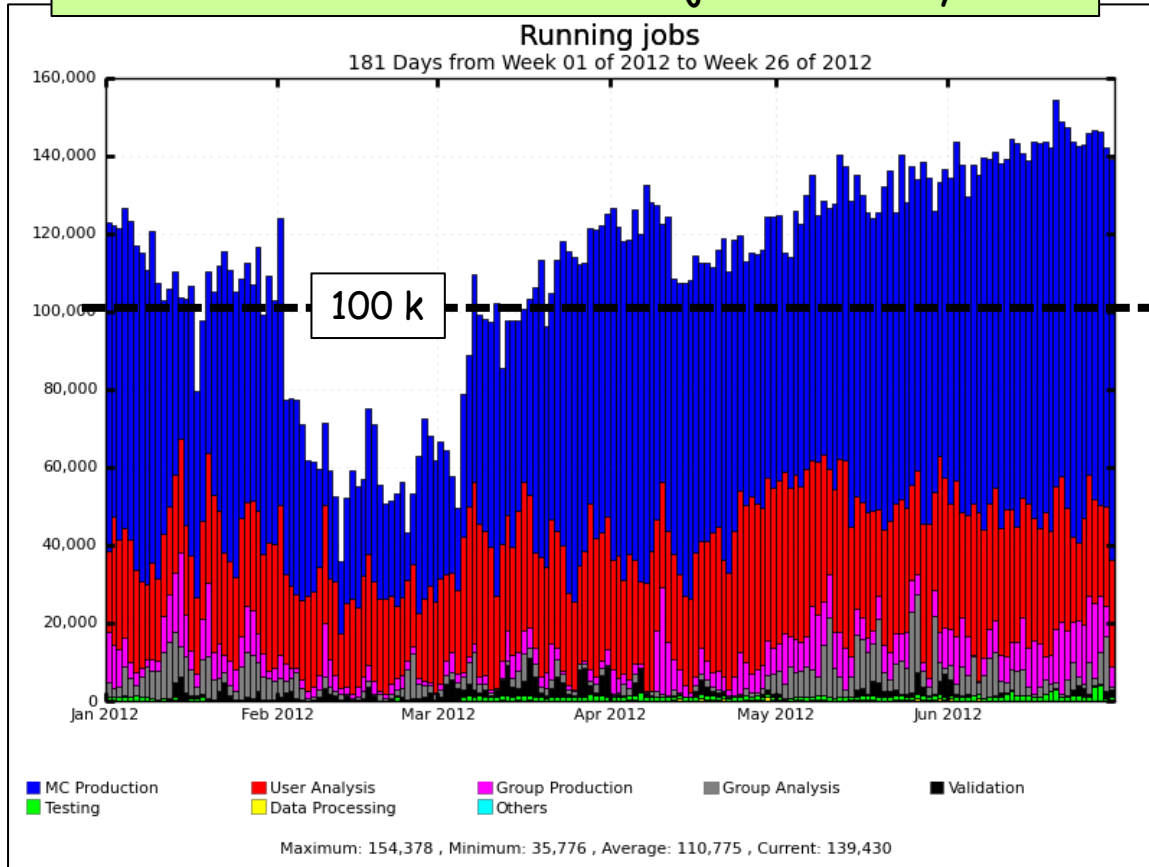
173 PB of storage

> 2 million jobs/day



It would have been impossible to release physics results so quickly without the outstanding performance of the Grid (including the CERN Tier-0)

Number of concurrent ATLAS jobs Jan-July 2012



Includes MC production, user and group analysis at CERN, 10 Tier1-s, ~ 70 Tier-2 federations → > 80 sites

> 1500 distinct ATLAS users do analysis on the GRID

- ❑ Available resources fully used/stressed (beyond pledges in some cases)
- ❑ Massive production of 8 TeV Monte Carlo samples
- ❑ Very effective and flexible Computing Model and Operation team → accommodate high trigger rates and pile-up, intense MC simulation, analysis demands from worldwide users (through e.g. dynamic data placement)

A wealth of knowledge

Academic
Training
program

Summer
Student
program

Physics
and
computing
schools

Technical
Training
program

CERN
Teacher
schools

Outreach
programs

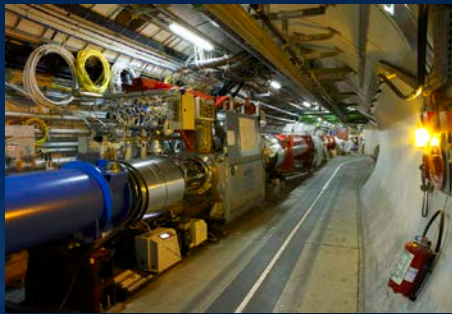
EU FP7
programs



Andrzej Nowak - The search for the Higgs Boson at CERN - Is there such a thing as too much compute power?

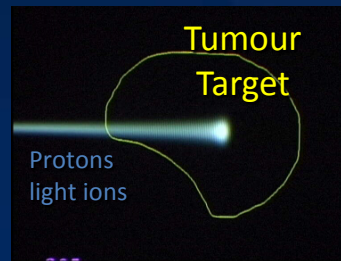
Innovation in science

Medical Applications as an Example of Particle Physics Spin-off

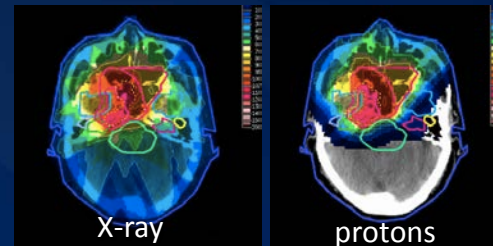


Accelerating particle beams
~30'000 accelerators worldwide
~17'000 used for medicine

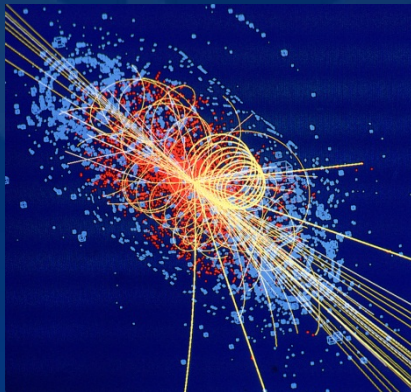
↔ Hadron Therapy



>70'000 patients treated worldwide (30 facilities)
>21'000 patients treated in Europe (9 facilities)



Leadership in Ion Beam Therapy now in Europe and Japan



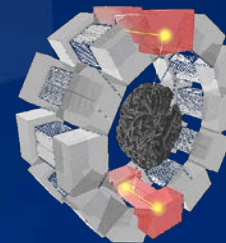
Detecting particles

↔ Imaging

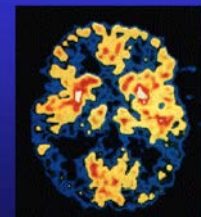
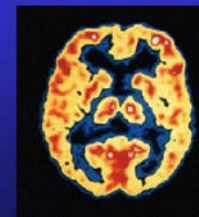
Clinical trial in Portugal for new breast imaging system (ClearPEM)



PET Scanner



Brain Metabolism in Alzheimer's Disease: PET Scan



Normal Brain

Alzheimer's Disease

Innovation in computing

1989: First high bandwidth transatlantic links

1999: The Grid vision materializes

2003: Several Internet2 land speed records

2012: LHC delivering intense data challenges

1991: The World Wide Web is born at CERN

2001: CERN wins Computerworld's 21st Century Achievement Award for SHIFT

2008: The WLCG is the world's largest grid

A European Cloud Computing Partnership: big science teams up with big business



Strategic Plan

- ▶ Establish multi-tenant, multi-provider cloud infrastructure
- ▶ Identify and adopt policies for trust, security and privacy
- ▶ Create governance structure
- ▶ Define funding schemes



To support the computing capacity needs for the ATLAS experiment

EMBL



Setting up a new service to simplify analysis of large genomes, for a deeper insight into evolution and biodiversity



To create an Earth Observation platform, focusing on earthquake and volcano research





Accelerating Science and Innovation

Continued support of the worldwide physics community and the European population

Great science and engineering + great partners = great innovation

Challenges in computing

Big(ger) Data

- LHC upgrades
- New paradigms, science

Exascale

- Computing evolution
- Next-gen interconnect

Society

- Scientific leadership
- Sustainable computing

Big(ger) data

Data rates at the LHC to increase by ~100x



“Sustainable computing”

Future directions in computing

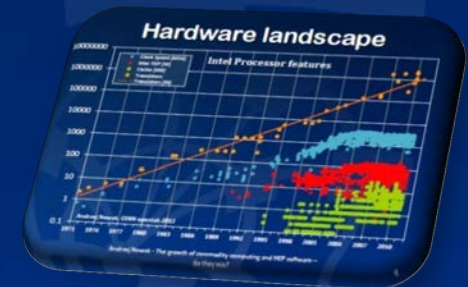
- **Software replacing hardware**
 - Programmability replaces rigid structures
- **Intensive compute**
 - Local farms must have much higher processing capacity
- **Accelerators**
 - Experiments with Intel MIC and GPUs
- **Silicon photonics**



Where will we be tomorrow?

	SIMD	ILP	HW THREADS	CORES	SOCKETS
MAX	8	4	1.25	12	4
TYPICAL	6	1.57	1.25	10	2
HEP	1	0.80	1.25	8	2

	SIMD	ILP	HW THREADS	CORES	SOCKETS
MAX	8	32	43.2	518.4	2073.6
TYPICAL	6	9.43	11.79	117.86	235.71
HEP	1	0.8	1	8	16



The CERN openlab

A unique research partnership of CERN and the industry

Objective: The advancement of cutting-edge computing solutions to be used by the worldwide LHC community

- Partners support manpower and equipment in dedicated competence centers
- openlab delivers published research and evaluations based on partners' solutions – in a very challenging setting
- Created robust hands-on training program in various computing topics, including international computing schools; Summer Student program
- Past involvement: Enterasys Networks, IBM, Voltaire, F-secure, Stonesoft, EDS; Future involvement: Huawei
- Now in phase IV: 2012-2014

<http://cern.ch/openlab>



PARTNERS

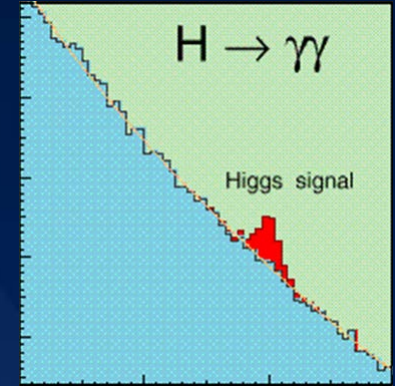


ORACLE

SIEMENS

Physics jobs

- **Independent events (collisions of particles)**
 - trivial (read: pleasant) parallel processing
- **Bulk of the data is read-only**
- **Very large aggregate requirements:**
 - computation, data, input/output
- **Chaotic workload**
 - research environment - physics extracted by iterative analysis:
Unpredictable, Unlimited demand
- **Compute power scales with combination of SPECint and SPECfp**
 - Good double-precision floating-point (10%-20% of total) is important!
 - Good transcendental math libraries needed
- **Key foundation: Linux together with GNU C++ compiler**



Where are we now? (software)

- **Large C++ frameworks with millions of lines of code**
 - Thousands of shared libraries in a distribution, gigabytes of binaries
 - Low number of key players but high number of brief contributors
- **Large regions of memory read only or accessed infrequently**
- **Characteristics:**
 - Significant portion of double precision floating point (10%+)
 - Loads/stores up to 60% of instructions
 - Unfavorable for the x86 microarchitecture (even worse for others)
 - Low number of instructions between jumps (<10)
 - Low number of instructions between calls (several dozen)
- **For the most part, code won't fit accelerators in its current shape**

Where are we now? (hardware)

- **Very limited or no vectorization**
 - Online has somewhat better conditions to vectorize
- **Sub-optimal instruction level parallelism (CPI at >1)**
- **Hardware threading unused, but often beneficial**
- **Cores used well through multiprocessing – bar the stiff memory requirements**
 - However, systems put in production with delays
- **Sockets used well**
- **Multiple systems used very well**
- **Relying on in-core improvements and # cores for scaling**

Where are we now?

	SIMD	ILP	HW THREADS	CORES	SOCKETS
TOP	4	4	1.35	8	4
OPTIMIZED	2.5	1.43	1.25	8	2
HEP	1	0.80	1	6	2

	SIMD	ILP	HW THREADS	CORES	SOCKETS
TOP	4	16	21.6	172.8	691.2
OPTIMIZED	2.5	3.57	4.46	35.71	71.43
HEP	1	0.80	0.80	4.80	9.60

Using a low single digit percentage of raw machine power available today

_%

Write your
percentage here



The Platform Competence Center

Focus on efficient computing



Close collaboration with the Physics department

PCC - particular interests

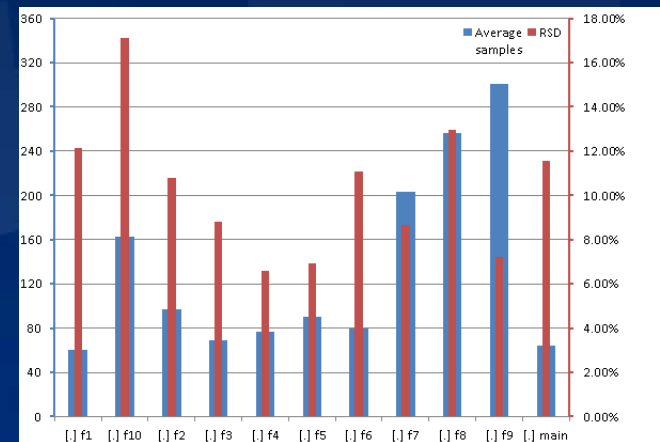
- **Compute optimization**
 - Absolute, per CHF, per Watt
 - Optimization tools
- **Compilers**
- **Parallelization**
 - x86 compatible technologies spanning the whole range from OpenCL to MPI
- **Accelerators**
 - Intel MIC, limited interest in GPUs, combos
- **Storage**
- **Next phase V in 2015-2018 (Exascale era)**

Compiler project

- **Main emphasis is on Intel64, even though some experiments still work in 32-bit mode for various reasons**
- **Regular benchmarking and evaluations**
 - **both compilers and hardware**
 - Long-term participation in the beta program
- **Closely following vectorization features and advanced optimization**

Tuning

- **Another long standing program**
 - Work with perfmon2, linux-perf
- **Alpha-tested new “XE” tools**
 - Feedback implemented in products
 - Still in the program
- **PMU research with Intel Israel**
- **Own profiling tools**



Parallelization

- 6 major collaborations at CERN still run heavily object oriented, sequential code
- High level problems are more painful than technical ones
 - How do you create a working collaboration?
 - Which model to choose?
 - Who is responsible for what?

Parallelization

- We now have a very practical understanding of various options for parallelism on x86
- Extensive whitepaper published, based on 2 years of research – as a part of the collaboration with Intel SSG
- Optimization of the MLFit application with:
 - TBB
 - MPI
 - Cilk
 - OpenMP
 - Vectorization (both autovectorization and CEAN explicit syntax)



Comparison of Software Technologies for Vectorization and Parallelization

Sverre Jarp, Alfio Lazzaro, Andrzej Nowak, Liviu Valsan
CERN openlab, September 2012 – version 1.0
White-paper as part of the collaboration between CERN openlab and Intel SSG

Executive Summary

This paper demonstrates how modern software development methodologies can be used to give an existing sequential application a considerable performance speed-up on modern x86 server systems. Whereas, in the past, speed-up was directly linked to the increase in clock frequency when moving to a more modern system, current x86 servers present a plethora of “performance dimensions” that need to be harnessed with great care. The application we used is a real-life data analysis example in C++ analyzing High Energy Physics data. The key software methods used are OpenMP, Intel Threading Building Blocks (TBB), Intel Cilk Plus, and the auto-vectorization capability of the Intel compiler (Composer XE). Somewhat surprisingly, the Message Passing Interface (MPI) is successfully added, although our focus is on single-node rather than multi-node performance optimization. The paper underlines the importance of algorithmic redesign in order to optimize each performance dimension and links this to close control of the memory layout in a thread-safe environment. The data fitting algorithm at the heart of the application is very floating-point intensive so the paper also discusses how to ensure optimal performance of mathematical functions (in our case, the exponential function) as well as numerical correctness and reproducibility. The test runs on single-, dual-, and quad-socket servers show first of all that vectorization of the algorithm (with either auto-vectorization by the compiler or the use of Intel Cilk Plus Array Notation) gives more than a factor 2 in speed-up when the data layout in memory is properly optimized. Using coarse-grained parallelism all three approaches (OpenMP, Cilk Plus, and TBB) showed good parallel speed-up on the available CPU cores. The best one was obtained with OpenMP, but by combining Cilk Plus and TBB with MPI in order to tie processes to sockets, these two software methods nicely closed the gap and TBB came out with a slight advantage in the end. Overall, we conclude that the best implementation in terms of both ease of implementation and the resulting performance is a combination of the Intel Cilk Plus Array Notation for vectorization and a hybrid TBB and MPI approach for parallelization.

Intel MIC at openlab

Early access

- Work since MIC alpha (under RS-NDA)
- ISA reviews in 2008

Results

- 3 benchmarks ported from Xeon and delivering results: ROOT, Geant4, ALICE HLT trackfitter

Expertise

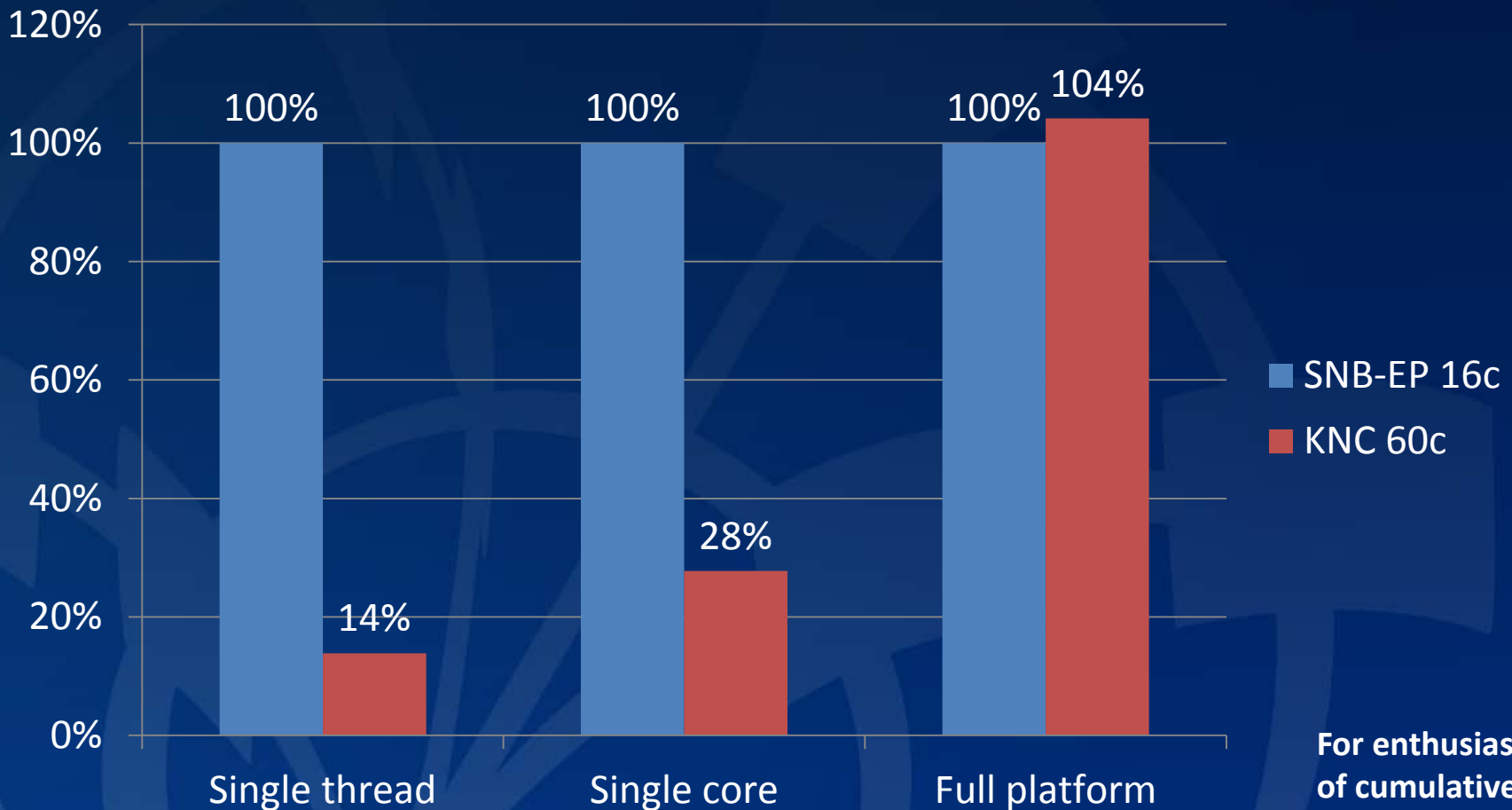
- Understood and compared with Xeon
- **Post-launch dissemination**

Intel MIC

- **ALICE/CBM track fitter prototype**
 - Threaded
 - Explicitly vectorized, vectors wrapped in C++ classes
- **MLFit**
 - Threaded (pthreads, MPI, OpenMP, TBB)
 - Vectorized (Cilk+)
- **Early multi-threaded Geant4 prototype**
 - Threaded (pthreads)
 - **No vectorization**
- **Test hardware**
 - Pre-production Knights Corner – 61 cores @ 1.1 GHz
 - Sandy Bridge-EP – 16 cores @ 2.7 GHz, Turbo on
 - Frequency unscaled results reported (1:1 comparison)

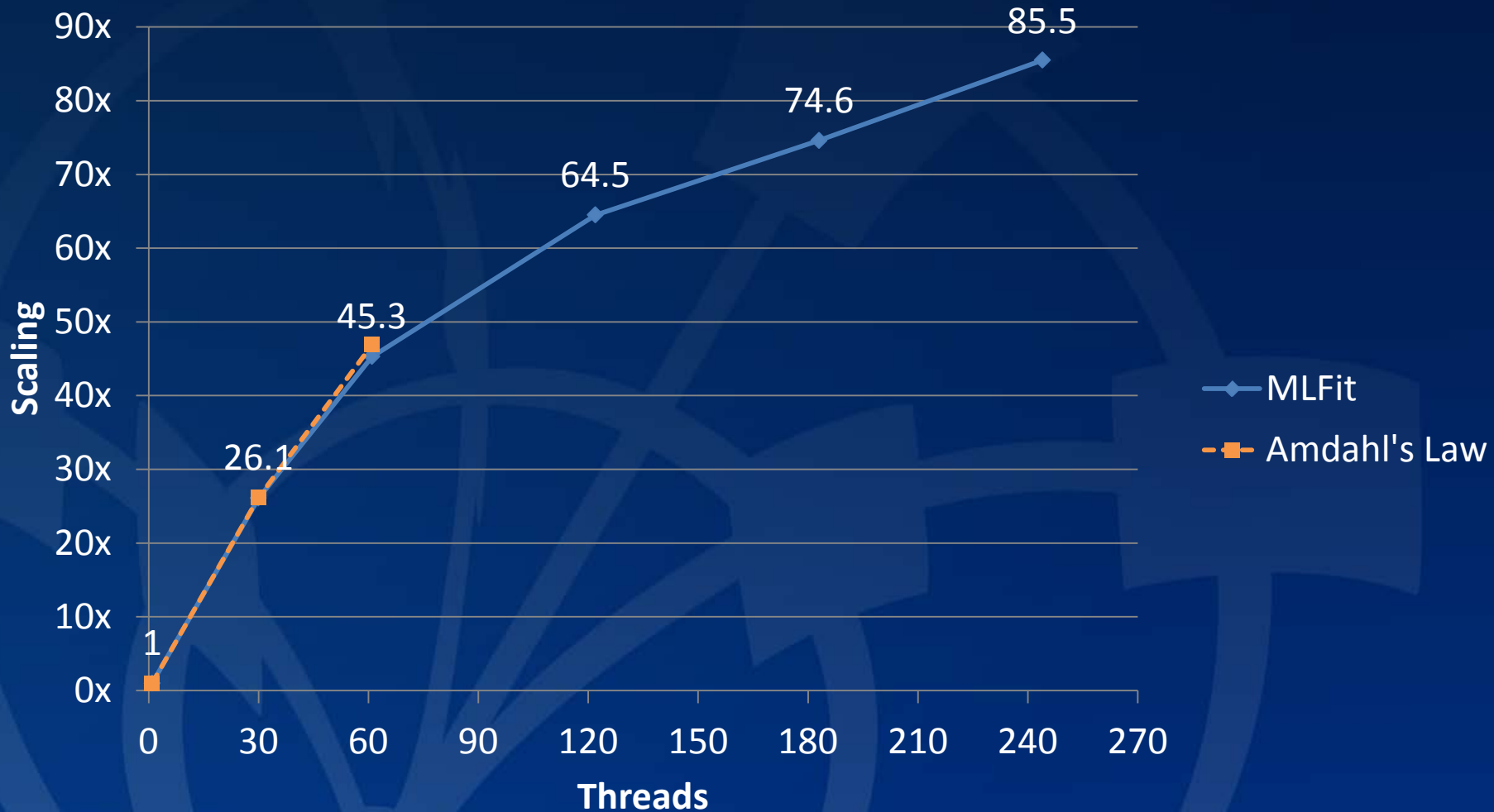
MLFit performance

OpenMP, no block splitting, higher is better



For enthusiasts
of cumulative
speed-up: 34x

MLFit scaling (OpenMP)

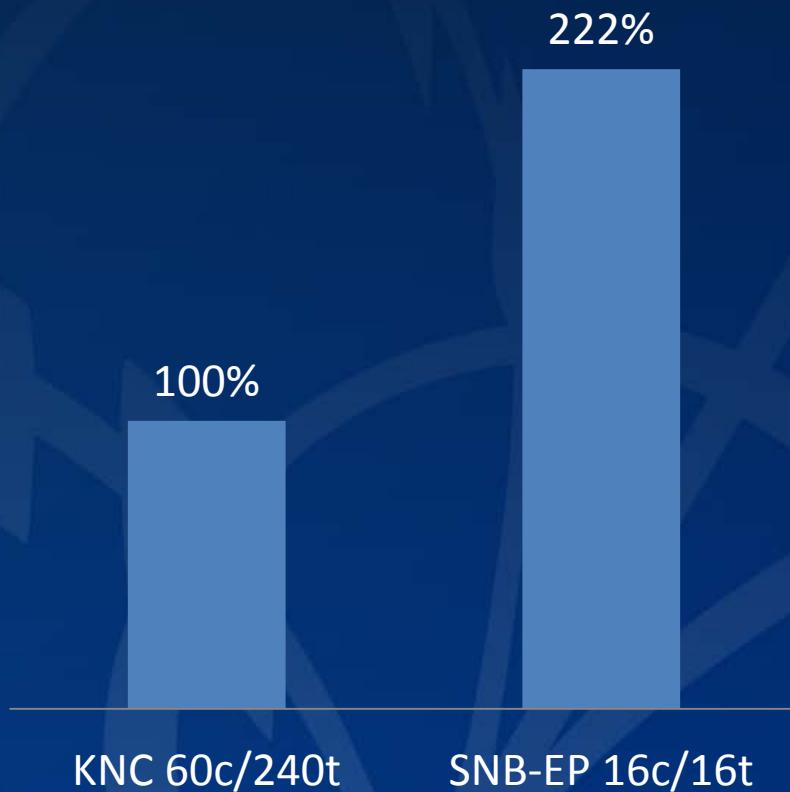


Andrzej Nowak - The search for the Higgs Boson at CERN - Is there such a thing as too much compute power?

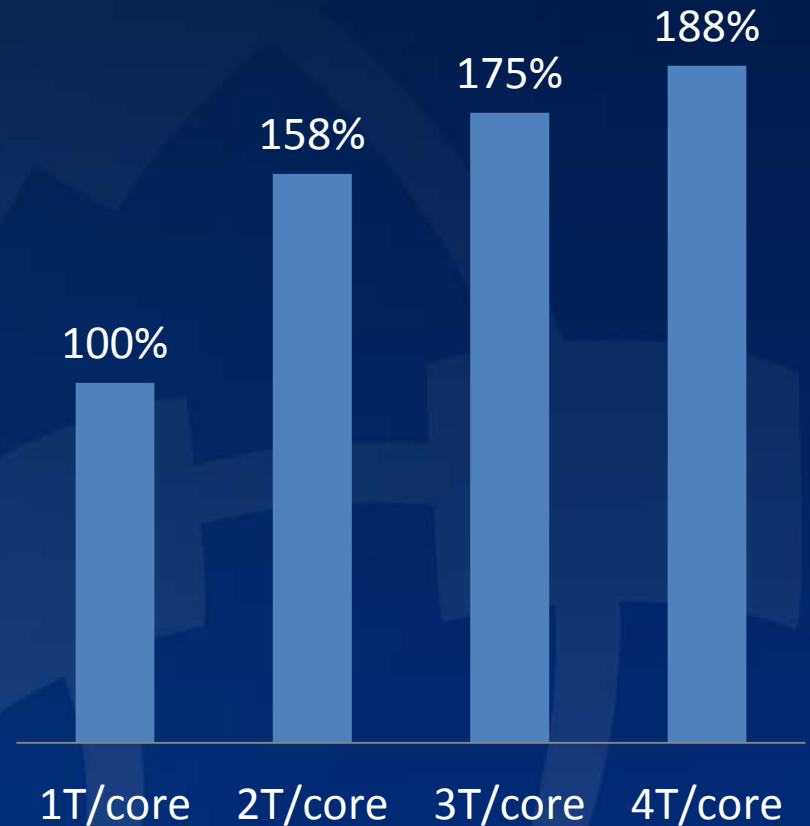
MTG4 performance

(higher is better, no vectorization)

Full platform performance



HW threading throughput



MTG4 – example profile

Function / Call Stack	CLK %	INST %
sqrt	14.35%	22.16%
exp	6.47%	9.47%
atan2	4.22%	6.31%
CLHEP::RanluxEngine::flat	3.24%	5.60%
G4ElasticHadrNucleusHE::HadronNucleusQ2_2	3.01%	2.41%
G4PhysicsVector::Value	2.76%	0.95%
log	2.22%	2.85%
G4VoxelNavigation::LevelLocate	2.05%	0.66%
G4VoxelNavigation::ComputeStep	1.64%	1.10%
G4ClassicalRK4::DumbStepper	1.59%	2.96%
G4SteppingManager::DefinePhysicalStepLength	1.54%	1.39%
G4Navigator::ComputeStep	1.40%	1.01%

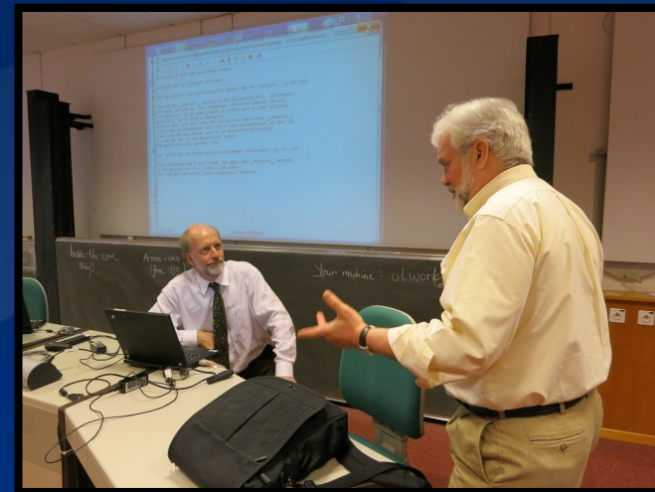
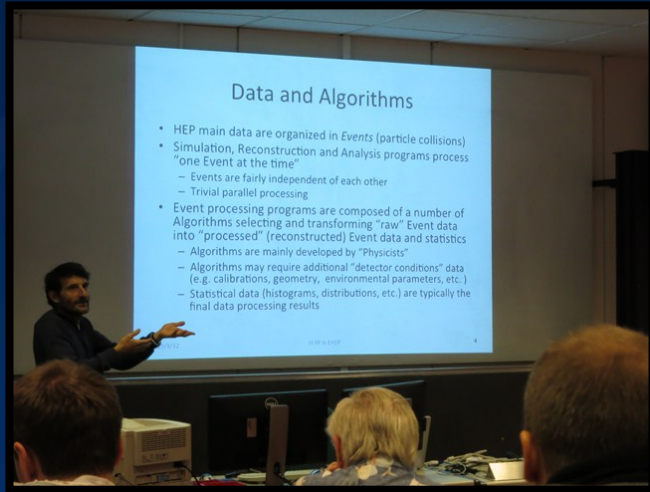
Teaching

- International computing schools
- Workshops
 - 10 workshops in 2012
 - >350 participants



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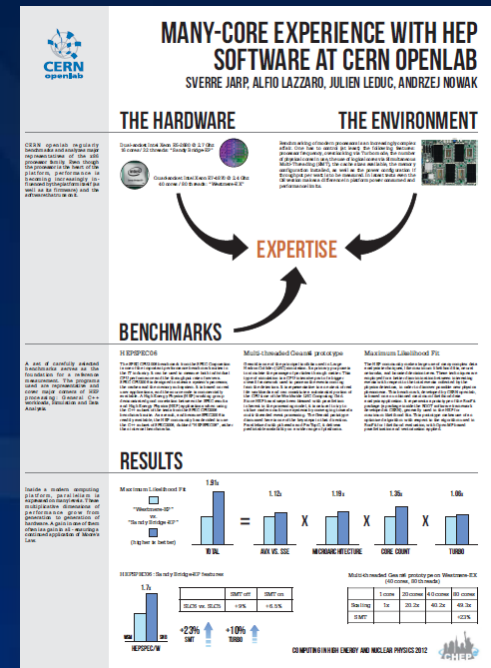
Numerical computing workshops



Andrzej Nowak - The search for the Higgs Boson at CERN - Is there such a thing as too much compute power?

Dissemination

- International conferences
- CERN whitepapers
- CERN-Intel whitepapers
- Industry events
 - ISC 2010 keynote with Kirk Skaugen
 - IDF 2011 keynote with Justin Rattner
 - ERIC 2012 keynote (Andrzej Nowak)



Dissemination



Kirk B. Skaugen
Vice President, Intel Architecture Group &
General Manager, Data Center Group, USA
HPC Technology Scale-Up & Scale-Out
(International Supercomputing Conference
2010 / 30.05.2010)



Sverre Jarp
Chief Technical Officer



Andrzej Nowak - The search for the Higgs Boson at CERN - Is there such a thing as too much compute power?

ICE-DIP

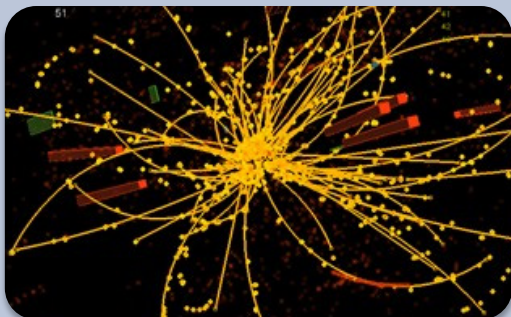
- EU Framework Program 7 project looking for (amongst other things) efficient methods of accelerator/co-processor use
- Focus on data taking past 2016
- Of particular interest
 - Getting data into the platform
 - Getting data into the accelerator/co-processor
 - Efficient processing
 - Efficient distribution of results
- What role for software?
- Funded



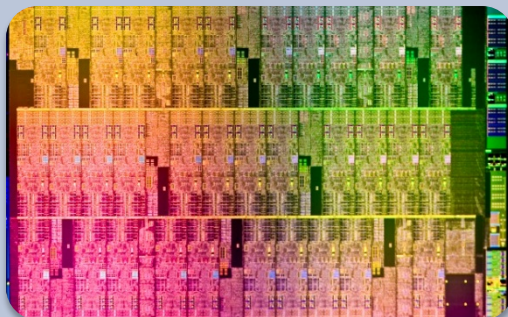
BIG-DATA

- **How can Big Science work with Big Data?**
- **Planned project:**
 - 16 partners
 - Foresees 14 new personnel spread across multiple labs
 - 4 years
- **Another related collaboration proposal submitted to Intel**
- **Funding decision in Spring**

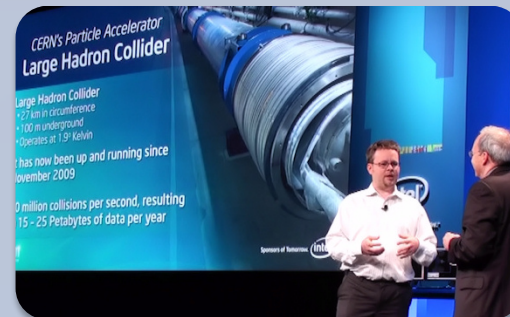
Summary



New
challenges
ahead of
CERN



Upcoming
new era
for LHC
computing



openlab
and Intel
actively
contribute

THANK YOU

Q & A



Questions? Andrzej.Nowak@cern.ch